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Assistant Commissioner for Patents Box PATENT APPLICATION Washington, D.C. 20231

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Transmitted herewith for filing is the patent application of

Inventor(s): LIU, Meng-Huang

LAI, Chun-Hsiang; SU, Sing

LU, Tao-Cheng

ESD PROTECTION CIRCUIT FOR MULTI-POWER AND MIXED-VOLTAGE For:

INTEGRATED CIRCUIT

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<u>X</u>	A specification consisting of <u>12</u> pages
<u>X</u>	06 sheet(s) of formal drawings
<u>X</u>	An assignment of the invention - \$40.00 Recording Fee
	Certified copy of Priority Document(s)
<u>X</u>	Executed Declaration X Original Photocopy
	Applicant claims small entity status in accordance with 37 CFR 1.27
	Application Data Sheet in accordance with 37 C.F.R. 1.76

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Prelimin	ary Amendment
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Information Disclosure Statement, PTO-1449 and reference(s)

___ Other ____

____ Applicant requests early publication

The filing fee has been calculated as shown below:

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SMALL ENTITY

FOR	NO.	FI	LED		NO.	EXTRA	RAT	ΓE	FEE		F	(TAS	Ξ	FEE
BASIC FEE	****	**	***:	* *	***	* * * * * * * * * * * * * * * * * * * *	****	*	\$710.00	or	**	***		\$355.00
TOTAL CLAIMS	7	-	20	=		0	x18	=\$	0.00	or	x	9	=	\$ 0.00
INDEPENDENT	2	_	3	=		0	x80	=\$	0.00	or	×	40	=	\$ 0.00
MULTIPLE DEPENDENT CLAIM PRESENTED <u>no</u>					+270) =	\$ 0.00	or	+1	35	=	\$ 0.00		

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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

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